

METHOD AND SYSTEM FOR GENERATING A DESIGN-SPECIFIC TEST CASE FROM A GENERALIZED SET OF BUS TRANSACTIONS

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ABSTRACT OF THE DISCLOSURE

A method and system for automating the creation of test cases for logic designs. A comprehensive set of bus transactions characterizing a bus architecture is provided to a test case designer in a user interface. The designer may enter inputs corresponding to a particular design-under-test (DUT) via the interface. The interface processes the inputs to automatically generate a configuration file corresponding to the particular DUT. The configuration file may be processed by a generator program to automatically generate a test case comprising one or more bus transactions customized to the particular DUT.

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